



**SHRI SITARAMBHAJI NARANJI PATEL INSTITUTE OF
TECHNOLOGY AND RESEARCH CENTRE, UMRAXH**

WEBINAR ON

“Career Opportunities and Challenges in VLSI Industry”

Webinar Details:

Date: 12 th May 2020	Department: Electronics and Communication Engineering	Number of participants: 40
Topic: Career Opportunities and Challenges in VLSI Industry		
Expert: Ms. Chandni Dodiya, Design Verification Engineer, Marvell Semiconductor, Pune.		
Objective: To Create awareness about jobs in VLSI Industry		

Training & Placement cell along with Electronics and Communication Engineering Department of S. N. Patel Institute of Technology & Research Centre, Umrakh has organized webinar on “**Career Opportunities and Challenges in VLSI Industry**” on **12th May 2020**. In this webinar around 40 participants were presents through zoom online meeting. Webinar was delivered by Ms. Chandni Dodiya. She is Design Verification Engineer Marvell Semiconductor, Pune. She has more than 2 years of experience as an ASIC Verification Engineer at Wipro. During webinar participates had grab the knowledge about growth of VLSI industry, different job profiles and skills required to for VLSI industry.

Webinar Photographs



S. N. Patel Institute of Technology & Research Centre, Umrah
(A Vidyabharti Trust Institution, Umrah)
Electronics & Communication Engineering Department

Webinar on “Career Opportunities and Challenges in VLSI Industry”



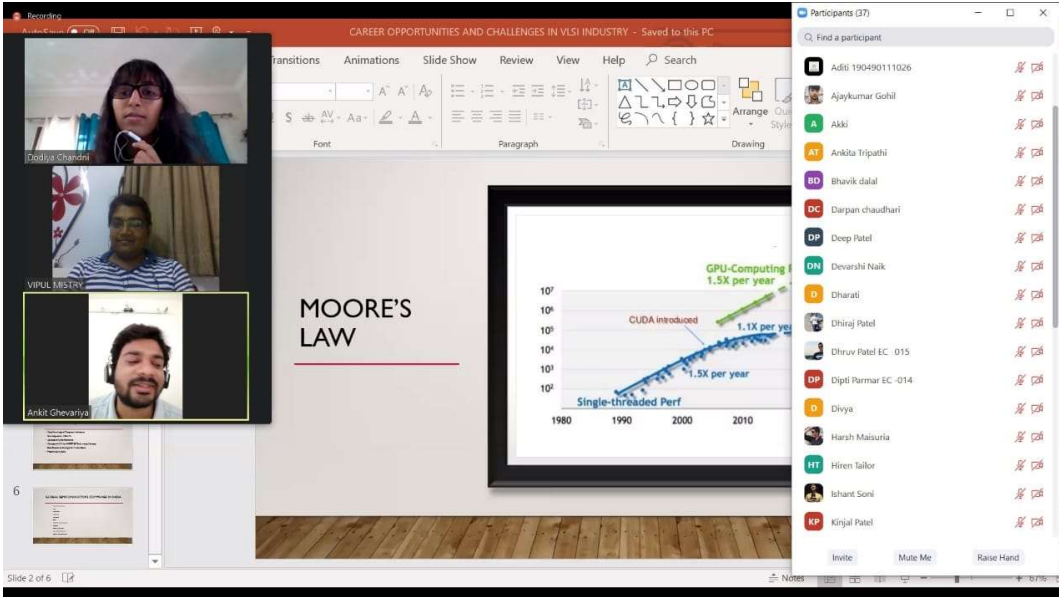
Speaker
Ms. Chandni Dodiya
Design Verification Engineer
Marvell Semiconductor, Pune
More than 2 years of experience as an
ASIC Verification Engineer at Wipro

Date: 12th May 2020 | Time: 1:00 p.m.

JOIN ONLINE ON ZOOM:
Meeting ID: 6191094119
Password: 034481

Organized by:
Training & Placement Cell, SNPIT&RC

Vidyabharti Campus, At & Po Baben, Ta: Bardoli, Dist: Surat, Pin: 394601 Contact No. 9925 777 117



The screenshot shows a Zoom webinar interface. The main content area displays a slide titled "MOORE'S LAW" with a line graph. The graph plots performance growth from 1980 to 2010 on a logarithmic scale. Two lines are shown: "Single-threaded Perf" (blue) and "GPU-Computing" (green). Both lines show a growth rate of 1.5X per year. A vertical line marks "CUDA introduced" around 2006. The "GPU-Computing" line starts around 2006 and continues to 2010. The "Single-threaded Perf" line starts around 1980 and continues to 2010. The graph shows that GPU computing performance growth has surpassed single-threaded performance growth since the introduction of CUDA. On the left, three video thumbnails are visible for participants: Pooja Chaudhri, Virul Erisw, and Ankit Ghevariya. On the right, a "Participants (37)" list is shown with names and icons for various attendees.