

**Electrical Engineering Department**

Semster : 3

Electrical- A

Term: Winter 24

Location : W-1

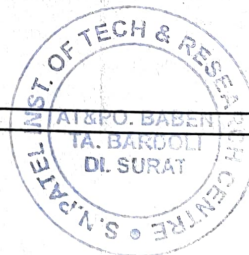
Time	Monday	Tuesday	Wednesday	Thursday	Friday
9:30 to 10:20	ADE MAP	ADE MAP	CST RHH	AMEE HAD	IC TNC
10:20 to 11:10	AMEE TBP	AMEE TBP	ADE VRP	ETC PSP	ETC PSP
11:10 to 12:00	CST NKP	ECA BDP	AMEE HAD	ADE VRP	CST NKP
12:00 to 12:50	Lunch Break				
12:50 to 1:40	ECA VBP	DE -1 A PKC	LIBRARY	CST RHH	AMEE TBP HAD SAS
1:40 to 2:30	ECA VBP		LIBRARY	ECA BDP	
2:30 to 2:45	Break				
2:45 to 3:35	ETC PSP	CST / TNC Batch - A-1 / Loc - B-8/Comp ECA / BDP Batch - A-2 / Loc - B-6/2	ADE / MAP Batch - A-1 Loc - B-12 L CST / NKP Batch - A-2 / Loc - B-8/Comp	ECA / BDP Batch - A-1 / Loc - B-6/2 ADE / MAP Batch - A-2 Loc - B-12 L	IC KHC
3:35 to 4:25					LIBRARY

Subject Details		Faculty Details			Location
Subject Code	Name	Initial	Name		
3130008	Design Engineering - 1 A	PKC	Prof. Pratap K Chaini		B 11
3130004	Effective Technical Communication	PSP	Prof. Purva S. Patel		B 15
3130007	Indian Constitution	KHC	Prof. Kinjal H. Chaudhari		B 8
		TNC	Prof. Trisha N. Chaudhari		B 8
3130905	Control System Theory	RHH	Dr. Ravish H. Hirpara		B 6/2
		NKP	Prof. Nisarg K Patel		B 8
3130906	Electrical Circuit Analysis	VBP	Prof. Vinod B Prajapati		B 6/1
		BDP	Prof. Brijesh D Parmar		B 11
3130907	Analog & Digital Electronics	MAP	Prof. Mavank A Patel		B 13
		VRP	Prof. Virang R Patel		B 13
3130908	Applied Mathematics for Electrical Engineering	TBP	Prof. Tejas B Patel		B 15
		HAD	Prof. Hetali A Dhimmar		B 15
		SAS	Prof. Seemabano A. Shah		B 15
Batch Information:		BATCH A-1	230490109002 TO 230490109015		
		BATCH A-2	230490109016 TO 230490109030		

Time Table Coordinator

HOD

Principal



S N Patel Institute of Technology & Research Center, Umrahkh

Electrical Engineering Department

Semster : 3

Electrical- **B**

Term: Winter 24

Location : W-7

Time	Monday	Tuesday	Wednesday	Thursday	Friday
9:30 to 10:20	AMEE TBP	AMEE TBP	CST NKP	ADE VRP	CST RHH
10:20 to 11:10	CST NKP	ETC PSP	ADE MAP	IC TNC	ADE MAP
11:10 to 12:00	ADE VRP	IC KHC	AMEE KJP	ECA BDP	AMEE KJP
12:00 to 12:50	Lunch Break				
12:50 to 1:40	CST / NKP Batch - B-1 / Loc - B-8/Comp ADE / MAP Batch - B-2 Loc - B-12 L	ECA VBP	ETC PSP	ETC PSP	ECA BDP
1:40 to 2:30		ECA VBP		CST RHH	LIBRARY
2:30 to 2:45	Break				
2:45 to 3:35	ECA / BDP Batch - B-1 / Loc - B-6/2 CST / NKP Batch - B-2 / Loc - B-8/Comp	LIBRARY	DE - I. A PKC	AMEE MNZ KJP TBP	ADE / MAP Batch - B-1 Loc - B-12 L ECA / BDP Batch - B-2 / Loc - B-6/2
3:35 to 4:25		LIBRARY			

Subject Details		Faculty Details		Location
Subject Code	Name	Initial	Name	
3130008	Design Engineering - I A	PKC	Prof. Pratap K Chaini	B 11
3130004	Effective Technical Communication	PSP	Prof. Purva S. Patel	B 15
3130007	Indian Constitution	KHC	Prof. Kinjal H. Chaudhari	B 8
		TNC	Prof. Trisha N. Chaudhari	B 8
3130905	Control System Theory	RHH	Dr. Ravish H. Hirpara	B 6/2
		NKP	Prof. Nisarg K Patel	B 8
3130906	Electrical Circuit Analysis	VBP	Prof. Vinod B Prajapati	B 6/1
		BDP	Prof. Brijesh D Parmar	B 11
3130907	Analog & Digital Electronics	MAP	Prof. Mayank A Patel	B 13
		VRP	Prof. Virang R Patel	B 13
3130908	Applied Mathematics for Electrical Engineering	MNZ	Dr. Mansi N. Zaveri	B 10
		KJP	Prof. Khyati J Patel	B 23
		TBP	Prof. Tejas B Patel	B 15
Batch Information:		BATCH B-1		
		BATCH B-2		

Time Table Coordinator

MOD

Principal

